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LSI LOGIC CORPORATION Office of General Counsel 1621 Barber Lane M/S D-106 Milpitas, CA 95035

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Date:

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09/842,214

Number of Pages Including this Page

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US Serial No.: 09/842,214 Filing Date: April 25, 2001

Group Art Unit:

Docket No: 03-2459/P033

Examiner:

Request for Status

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TRANSMITTAL			Application Number	09/842,214
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(10 be accorded an obligation of and			Group Art Unit	
			Examiner Name	
Total number of page	es in this submiss	lon 2	Attorney Docket Number	P033 / 03-2459
ENCLOSURES (check all that apply)				
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Individual Name Leo Peters, Reg. No. 33,562, Phone: [+1] 408-433-4578				
Signature			-	
Date 5-5-04				
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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope to: Assistant Commissioner for Patents, Washington, D.C. 20231 on this date: May 5, 2004				
Typed or printed name Manu Kashyap, Phone: [+1] 408-433-7475				
Signature Date 5/5/04				5/5/04
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE CEIVED

In re Application of:

Dimitri Vezenov et. al.

Serial No.:

09/842,214

Filed:

April 25, 2001

For:

Method of Fabricating Sub-Micron

Hemispherical and Hemicylidrical Structures from Non-Spherically

Shaped Templates

GENTRAL FAX CENTER

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Group Art Unit:

Examiner:

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Atty Docket:

P033 / 03-2459

STATUS REQUEST

Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

Sir:

Applicants request a status of the above referenced application. Please mail the status notification to the address stated below.

LSI Logic Corporation 1621 Barber Lane, MS D-106 Milipitas, CA 95035 [+1] 408-433-7475

Date: 5-5-64

Respectfully submitted,

Leo Peters, Ph: [+1] 408-433-7475

Reg. No. 33,562